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10593526 - GAI: 2472

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10593526	
	Filing Date		2007-07-24	
	First Named Inventor	Jean-Luc Soulard		
	Art Unit	2472		
	Examiner Name	Ashil S. Farahmand		
	Attorney Docket Number	PF040046		

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	1	6-162680	JP	A	1994-06-10	Silicon Systems Inc.	English Equivalent is US 5,311,178 A	<input type="checkbox"/>
	2	10-210019	JP	A	1998-08-07	Mitsubishi Electric Corp.	Machine Translation	<input checked="" type="checkbox"/>

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	3	11-177653	JP	A	1999-07-02	NEC Corp.	Machine Translation	<input checked="" type="checkbox"/>
	4	2001-028537	JP	A	2001-01-30	Victor Company of Japan	Machine Translation	<input checked="" type="checkbox"/>
	5	2001-177401	JP	A	2001-06-29	Mitsubishi Electric Corp.	Machine Translation	<input checked="" type="checkbox"/>
	6	2002-281077	JP	A	2002-09-27	Hitachi Ltd.	Machine Translation	<input checked="" type="checkbox"/>
	7	2003-258804	JP	A	2003-09-12	Matsushita Electric Ind Co Ltd.	English Equivalent is US 2003/0169777 A1	<input type="checkbox"/>
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Examiner Signature	/Ashil Farahmand/	Date Considered	07/27/2011
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	Art Unit	2472		
	Examiner Name	Ashil S. Farahmand		
	Attorney Docket Number	PF040046		

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☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

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SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Joel M. Fogelson/	Date (YYYY-MM-DD)	2011-07-22
Name/Print	Joel M. Fogelson	Registration Number	43,613

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